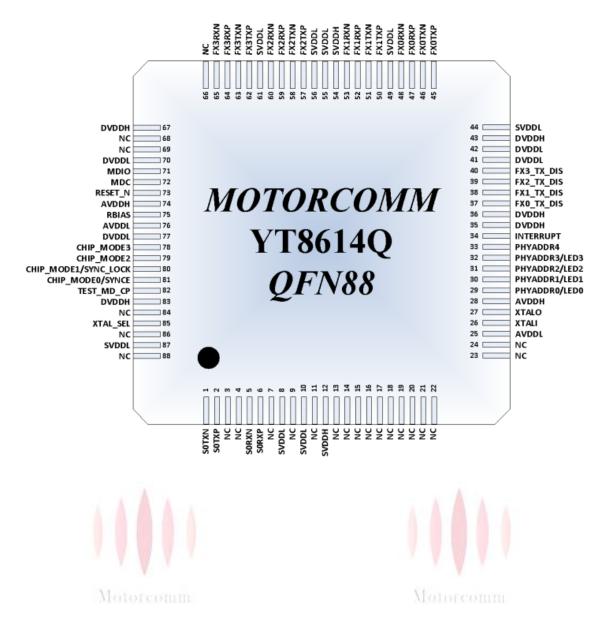# Motorcomm YT8614QC/YT8614QH Product Brief

QSGMII To Quad 1000BASE-X/100BASE-FX Transceiver

Overview

The YT8614Q is used for converting QSGMII interface to four 1000BASE-X/100BASE-FX ports. It's managemet interface comply with IEEE 802.3, Clause 22, thus the switch controller can access the related register to get the status and set configuration.
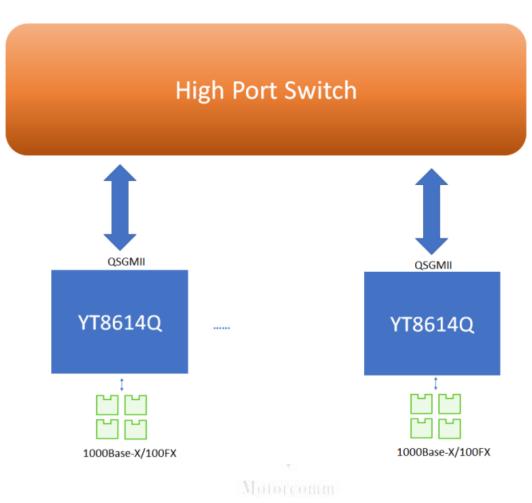
Pin Assignment

Application Diagram

Key Features

- 5Gbps QSGMII to x4 100BASE-FX/1000Base-X port conversion and each port can also be separately set to SGMII_MAC

- 5Gbps QSGMII to x4 SGMII_PHY port conversion

- 5Gbps QSGMII to x4 SGMII_MAC port conversion

- Supports unidirection in 1000BASE-X/100Base-FX

- SerDes Test pattern
    - PRBS-7/10/31
    - IDLE /K28.5 /D5.6
    - Customized define by user

- SerDes BIST

- Packet Generator and Checker

- Low power consumption

- Easy layout, good EMI/EMS, and good thermal performance

- Supports 25MHz or 50MHz reference clock input

- 3.3V and 1.2V power supply

- Sync-E clock output

- QFN-88 E-PAD package

Target Applications

- High Port Density Switch
- QSGMII TO 4*1000BASE-X/100BASE-FX Bridge
- QSGMII TO 4*SGMAC Bridge
- QSGMII TO 4*SGPHY Bridge